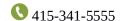
David Ljung Madison Stellar





Silicon Valley, CA

CAREER SUMMARY

CPU verification expert who can take on large verification tasks as well as toolchain creation, ready to fit in at your organization and make a difference.

Resume v5.4, 2021/01

**Resume v5.4, 20

SKILLS

Self-starter capable of writing everything from tools to test generators.

Computer Languages

Multi-linguistic: Ruby Porl, C. Java, Verilog, SystemVerilog, assembly, etc., Loften become the ruby/perl resource where awork.

ACCOMPLISHMENT.

Created entire custom verification toolchain for multiple companies.

Invented at original formal perificulty is shripted which were far beyond by inclustry far are and found all bugs in tested blocks in pre-chicolasses.

http://DaveSource.com/Successes/Formal-Blocks/

Have taken ownership of verifying complete GPU a multiplication in the companies, using techniques such as creating modular testbenches

Rewrote an entire post-silicon debug toolchain months ahead of schedule at a savings of \$3 million per day according to corporate calculations.

Specializes in creating state-of-the-art code generators such as a highly sophisticated reverse code packer for a custom VLIW CPU that <u>builds itself based off of parsing the specification</u>.

Accomplished at writing Directed-Random testbenches which provide over 95% of coverage.

Great at novel approaches to <u>breaking things</u>, which is why I've posted bugs in almost every tool I've used, including established tools such as compilers, linkers, shells, assemblers.

Designed/implemented custom shipping operations algorithms as a test pilot for DHL, accomplishing an order of magnitude improvement in runtime **and** savings.

Creator of shareware tools including 'album,' the world's #1 CLI album generator.

EMPLOYMENT / EDUCATION

Jan 2017 - Preser	nt <u>NVidia</u>	Senior GPU Verification Engineer
May 2015 - Jan 20	16 <u>Bluechip Systems</u>	Verification Consultant
Feb 2007 - Dec 20	013 iCelero, LLC (3Plus1 T	ech) Director of Verification
Jul 2002 - Preser	nt <u>DaveSource Consulting</u>	Head Engineer
Jan 2000 - Jul 20	02 <u>Transmeta Corp</u>	CPU Verification Engineer
Jul 1998 - Jan 20	000 SandCraft, Inc	CPU Verification Lead
Aug 1994 - Jun 19	998 Hewlett-Packard	CPU Verifcation and Debug

University of Wisconsin, Madison: Electrical Computer Engineering / Computer Science [GPA: 3.0]